

REMARKS

Claims 1-29 are pending and under consideration.

The rejections based on Kim are respectfully traversed. Claim 1, used as an example herein, recites a controller to detect the extracted horizontal synchronization signal from the digital video signals to determine a display mode, to output the first and second predetermined sampling clock signals to the signal converter and the scaler, respectively, according to the determined display mode, and to generate inverter on/off signals whenever the display mode is changed.

In contrast, Saito teaches turning a backlight lamp 5 off during either the horizontal or vertical synchronization period. Saito, col. 5, ln. 39-45. However, the vertical synchronization period does not correspond to the claimed display mode being changed. Instead, the synchronization period is a "blanking period", or the period when the video signal for a display does not appear. Saito, col. 1, ln. 66 to col. 2, ln. 3. However, there is no teaching that the display mode is changed while the video signal does not appear.

The remaining references do not overcome these deficiencies. Accordingly, withdrawal of the rejections is requested.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.


Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

Date: 11-12-08

By: 

Michael J. Badagliacca
Registration No. 39,099

1201 New York Avenue, N.W., 7th Floor
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501